**SRES’s**

**SANJIVANI COLLEGE OF ENGINEERING,**

**KOPARGAON 423 603 (M.S.)**

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**Department of**

**Electronics & Telecommunication Engineering**

**2018-2019**

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**PROJECT [STAGE-I] REPORT**

**ON**

**(NAME OF PROJECT IN CAPITAL ETTERS)**

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**(FONT SIZE:18)**

**CERTIFICATE**

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This is to certify that this report on project [Stage-I] entitled,

**“(TITLE OF YOUR PROJECT)”**

submitted by,

1. \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ [Examination seat number]
2. \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ [as per the sequence mentioned in final year
3. \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ project list]
4. \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

for the partial fulfillment of the requirements of Bachelor of Engineering (Electronics & Telecommunication Engineering) degree of the Savitribai Phule Pune University, Pune embodies the work done by them under our guidance and supervision in the academic year 2018-2019.

**(Prof. ---- ) (Prof. D. G. Lokhande)**

**Guide Coordinator**

**(Dr. B. S. Agarkar) (Dr. D. N. Kyatanavar)**

**Head of Dept Principal**

**ACKNOWLEDGEMENT**

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Students Name with serial No

(As per the sequence mentioned in final year project list)

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**CHAPTER 1**

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*PROJECT TITLE*

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| JULY | AUG | SEP | OCT | NOV | DEC | JAN | FEB | MAR | APR | MAY | JUN |
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| H/W  Design  Phase |  |  |  |  |  |  |  |  |  |  |  |  |
| PCB  Design |  |  |  |  |  |  |  |  |  |  |  |  |
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